Ref. No.: Date: 09-09-2025

Shadan College of Engineering and Technology

(An Autonomous Institution)
Peerancheru, Hyderabad – 500086, Telangana.

Department of Electronics and Communication Engineering

Agenda for the 5th Board of Studies (BoS) Meeting

Agenda for the Meeting:

I. To welcome the Board of Studies Members with a special welcome to the JNTUH nominee.

II. To Discuss the Course Structure and syllabus of B.Tech. (ECE) I Year (I & II SEM) and II Year (I & II SEM) under R-25 regulations.

III. To Discuss the courses offered by ECE department to other departments in B.Tech. I Year (I & II SEM) and II Year (I & II SEM) under R-25 regulations.

IV. To Discuss the Course Structure & Syllabi of M.Tech. (VLSI) I Year (I & II SEM) under R-25 regulations.

V. To Discuss the Course Structure & Syllabi of M.Tech. (ES) I Year (I & II SEM) under R-25 regulations.

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Members Present in the 5th BoS Meeting held on 09th Sept. 2025:

Sl No	Name of the Member	Designation	Category	Signature
01	Dr. Mohammad Iliyas	Dean Academics, Professor & Head of the Department	Chairperson	
02	Dr. A. Rajani	Professor of ECE, JNTUH UCESTH.	JNTUH Nominee	
03	Dr. M.S.S.Rukmini,	Professor in ECE, Dean Student Affairs, VFSTR, Guntur, A.P.	Subject Expert from outside the parent University	
04	Dr. Altaf C.	Associate Professor, ECE Department, Lords Institute of Engineering & Technology (Affiliated to OU)	Subject Expert from outside the parent University	
05	Dr. G. Ravi Kumar	Professor	Faculty member	
06	Dr. Shaik Saidulu	Professor	Faculty member	
07	Dr. Amairullah Khan Lodhi	Dean R&D, Professor	Faculty member	
08	Dr. Ravinder Korani	Professor, NSS Program Coordinator	Faculty member	
09	H.A. Abdus Samad	Associate Professor	Faculty member	
10	Katari Sampath Kumar	Assistant Professor	Faculty member	

11	V. Venkateshwarlu	Assistant Professor	Faculty member	
12	Imam Khasimbhi Shaik	Assistant Professor	Faculty member	
13	Salma Anjum	Assistant Professor	Faculty member	
14	Ravinder Gaja	Assistant Professor	Faculty member	
15	Khaja Fasiuddin	Assistant Professor	Faculty member	
16	Syed Tariq Ali Zaidi	Assistant Professor	Faculty member	
17	Avinash Yadlapati	Senior Manager, Intel Corporation Ltd.	Industry Representative	
18	Sharif Osaid Ahmed	Program Manager, Edventure Park Hardware Incubator, Hyderabad.	College Alumni	

MINUTES OF THE 5th MEETING OF THE BOARD OF STUDIES HELD AT 12.15 PM ON 09th Sept. 2025 AT THE SHADAN COLLEGE OF ENGINEERING AND TECHNOLOGY, PEERANCHERU, HYDERABAD-86.

Minutes of Meeting:

Item 1: The Chairperson welcomed all the Board of Studies Members with a special welcome to the JNTUH nominee Dr. A. Rajani, Professor of ECE, JNTUH UCESTH.

Resolution: The BoS noted and welcomed all the new Members.

Item 2: To Discuss the Course Structure & syllabi of B.Tech. (ECE) I Year (I & II SEM) R-25 regulations.

The Chairperson of BoS presented the detailed course structure & syllabi of B.Tech. I Year (I & II Sem) of R-25 regulations

Resolution: The members after thorough discussion recommended the course structure & syllabi of B.Tech. (ECE) I Year (I & II Sem). The details of the course structure are given in Annexure -1.

Item 3: To discuss the courses offered by ECE department to other departments in B.Tech I Year (I & II SEM) and II Year (I Sem & II Sem)

1. Electronic Devices and Circuits course along with the syllabus offered to I Year (I Sem) of CSE & CSE(AI&ML),
2. Electronic Devices and Circuits course along with the syllabus offered to I Year (II Sem) to CSE(DS), AI&ML, AI&DS, IT Department under R-25 regulations.
3. Electronic Devices and Circuits course along with the syllabus offered to II Year (I & II SEM) to EEE with EDC Lab (II-I), DE Lab (II-II), BE Lab (II-II) PCB Design (II-II) under R-25 regulations.

The Chairperson presented detailed syllabi of above said courses that are offered to CSE, CSE(AI&ML), CSE(DS), AI&ML, AI&DS, IT, EEE & ECE Departments under R-25 regulations.

Resolution: The members after thorough discussion Recommended the courses offered to CSE, CSE(AI&ML), CSE(DS), AI&ML, AI&DS, IT, EEE & ECE Department and the details are given in Annexure - 3.

Item 4: To discuss the Course Structure & syllabus of M.Tech. (VLSI) I Year (I & II SEM) R-25 regulations.

The Chairperson of BoS presented the detailed course structure of M.Tech (VLSI) I year (I & II Sem) of R-25 regulations.

Resolution: The members after thorough discussion recommended the course structure of M.Tech. (VLSI) I Year (I & II Sem). The details of the course structure are given in Annexure -4.

Item 5: To discuss the Course Structure & syllabus of M.Tech (Embedded Systems) I Year (I & II SEM) R-25 regulations.

The Chairperson of BoS presented the detailed course structure of M.Tech (ES) I Year (I & II SEM) of R-25 regulations.

Resolution: The members after thorough discussion recommended the course structure of M.Tech. (ES) I Year (I & II Sem). The details of the course structure are given in Annexure -4.

Attached course structure & syllabus is approved.

The session ended with profuse thanks by Chairperson to all the members for attending the meeting.

